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*Ade, M.; Lauwereins, R.; Peperstraete, J.A.;*  
Rapid System Prototyping, 1995. Proceedings., Sixth IEEE International Worksh  
7-9 Jun 1995  
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2 **GRAPE: a CASE tool for digital signal parallel processing**

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3 **Hardware-software trade-offs for emulation**

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4 **Buffer memory requirements in DSP applications**

*Ade, M.; Lauwereins, R.; Peperstraete, J.A.;*  
Rapid System Prototyping, 1994. 'Shortening the Path from Specification to Prot  
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